(12)
EUROPEAN PATENT APPLICATION

(72) Inventor: **Nakanishi, Hiroaki**
16-26, Moriyamacho-1-chome
Hitachi-shi(JP)

(72) Inventor: **Kuwahara, Hiroshi**
16-10, Omikacho-3-chome
Hitachi-shi(JP)

(72) Inventor: **Ide, Jushi**
473-44, Tonocho
Mito-shi(JP)

(72) Inventor: **Hirai, Koji**
2920-24, Mukaino
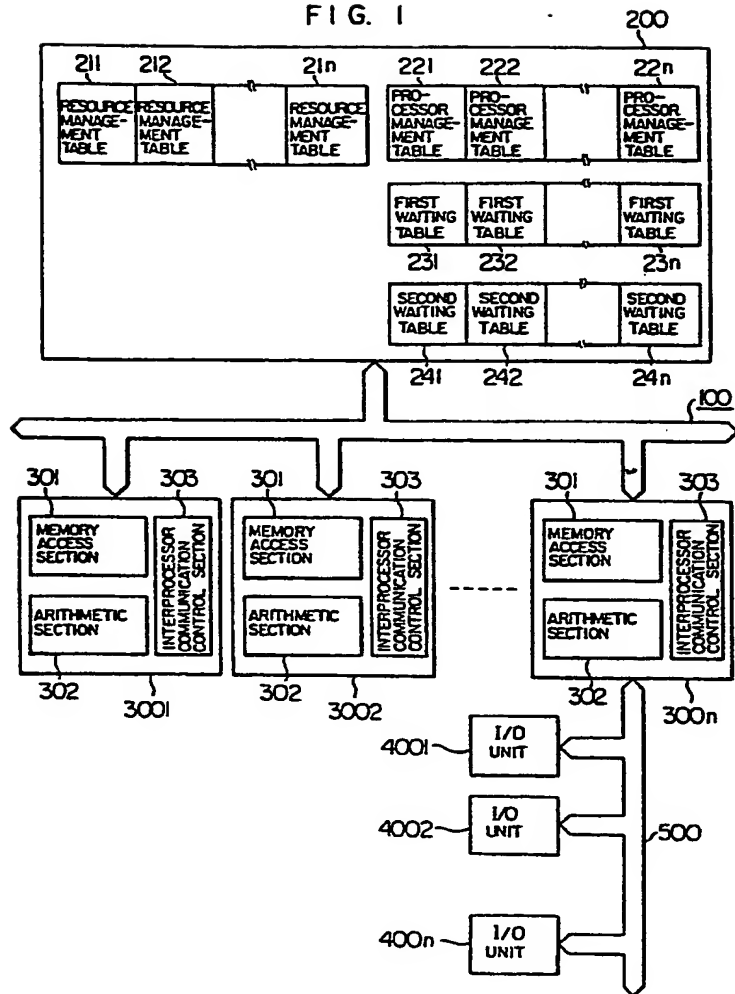**Mawatari Katsuta-shi(JP)**

(72) Inventor: **Kamiuchi, Toshiro**
540-40, Josuiminamicho
Kodaira-shi(JP)

(74) Representative: **Ebbinghaus, Dieter et al,**
K.L. Schiff Dr. A. von Funer Dipl.-Ing. P. Strehl Dr. U.
Schübel-Hopf Dipl.-Ing. D. Ebbinghaus Dr. Ing. D. Finck
Patentanwälte Mariahilfplatz 2 & 3
D-8000 München 90(DE)

(54) **Information processing system.**

(57) An operating system processor (3001) for managing resources concentrically, job processors (3002, ..., 300n) for executing processes input/output processors (4001, 4002, ..., 400n) for executing input/output processes, a file processor for executing input/output processing to a file system, and a main memory (200) are connected together by a common bus (100). The operating system processor (3001) receives a resource request from the job processors (3002, ..., 300n) so as to manage the allotment of resources to the job processors by referring to a resource management table (211, 212, ..., 21n), a processor management table (221, 222, ..., 22n), and waiting tables (231, 232, ., 23n, 241, 242, ..., 24n).

EP 0 049 521 A3

F I G. 1

200

211 212 21n 221 222 22n

RESOURCE MANAGE-MENT TABLE | RESOURCE MANAGE-MENT TABLE | RESOURCE MANAGE-MENT TABLE | PRO-CESSOR MANAGE-MENT TABLE | PRO-CESSOR MANAGE-MENT TABLE | PRO-CESSOR MANAGE-MENT TABLE

FIRST WAITING TABLE | FIRST WAITING TABLE | FIRST WAITING TABLE

231 232 23n

SECOND WAITING TABLE | SECOND WAITING TABLE | SECOND WAITING TABLE

241 242 24n

100

301 303 301 303 301 303

MEMORY ACCESS SECTION | INTERPROCESSOR COMMUNICATION CONTROL SECTION | MEMORY ACCESS SECTION | INTERPROCESSOR COMMUNICATION CONTROL SECTION | MEMORY ACCESS SECTION | INTERPROCESSOR COMMUNICATION CONTROL SECTION

ARITHMETIC SECTION | ARITHMETIC SECTION | ARITHMETIC SECTION

302 3001 302 3002 302 300n

4001 — I/O UNIT

4002 — I/O UNIT

500

400n — I/O UNIT

European Patent Office

EUROPEAN SEARCH REPORT

	DOCUMENTS CONSIDERED TO BE RELEVANT		
Category	Citation of document with indication. where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl. ³)
Y	IBM TECHNICAL DISCLOSURE BULLETIN vol.19, no 8, January 1977, NEW YORK USA; J.G.SAMS: "Low-cost systems using multiple processors", pages 2879-2883. *Page 2880, line 1 to page 2883, line 3*	1,2	G 06 F 9/46
Y	NEC RESEARCH and DEVELOPMENT no.36 January 1975, (TOKYO); KENI OHMORI et al.: "MICS-A MULTI-MICROPROCESSOR SYSTEM" pages 32-36. *Page 33, right-hand column, line 38 to page 35, left-hand column, line 15*	1	
Y	A.F.I.P.S. CONFERENCE PROCEEDINGS vol.40, SPRING JOINT COMPUTER CONFERENCE, NEW YORK USA, 1972; DAVIS et al.: "A building block approach to multiprocessing", pages 685-703. *Page 691 left-hand column, line 6 to page 703 left-hand column, line 32*	1,2	TECHNICAL FIELDS SEARCHED (Int Cl ³) G 06 F 9/46

The present search report has been drawn up for all claims

Place of search THE HAGUE	Date of completion of the search 30-03-1982	Examiner LEPEE W.

BEST AVAILABLE COPY

BAD ORIGINAL